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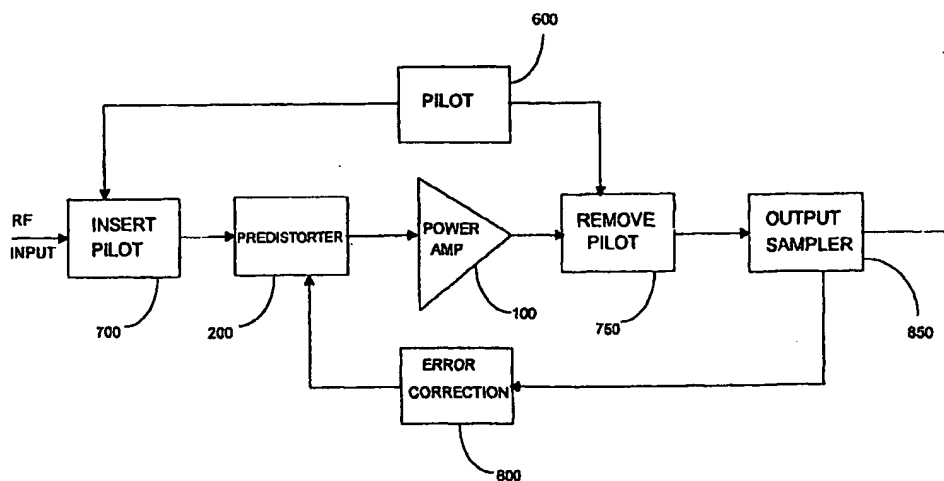
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<p>(21) International Application Number: PCT/GB99/02110 (22) International Filing Date: 2 July 1999 (02.07.99) (30) Priority Data: 9814391.0 2 July 1998 (02.07.98) GB (71) Applicant (for all designated States except US): WIRELESS SYSTEMS INTERNATIONAL LTD. [GB/GB]; Innovation House, Bristol Business Park, Coldharbour Lane, Bristol BS16 1EJ (GB). (72) Inventor; and (75) Inventor/Applicant (for US only): KENINGTON, Peter [GB/GB]; Trap Farm, Devauden Green, Chepstow NP6 6PE (GB). (74) Agents: HOGG, Jeffrey, Keith et al.; Withers & Rogers, Goldings House, 2 Hays Lane, London SE1 2HW (GB).</p>		<p>(81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).</p> <p>Published With international search report.</p>

(54) Title: A PREDISTORTER



(57) Abstract

A predistorter arrangement for linearising an amplifier. The predistorter arrangement is designed to compensate for IMD distortion in an amplifier using polynomial approximation techniques. The predistorter arrangement also includes pilot based feedback circuitry for controlling the operation of the predistorter.